# Datasheet

# SK305 *Aigoual* Linear TEC Driver

## **SK-Series Modules**

### **Features**

- +  $\pm 1 \, \text{A}$  true current source w/ digital control
- Current limitation
- Compliance voltage monitoring
- Bandwidth  $\geq 10 \, \mathrm{kHz}$
- Noise density  $\leq 1\,\mu\text{A}/\text{rtHz}$ , RMS  $\leq 100\,\mu\text{A}$
- Parallel operation for higher current
- Analog design Free of digital noise
- Remote interface
- Platform and stand-alone operation

## Applications

- Temperature control of laser diodes
- Laser frequency stabilization
- Optical phase-locked laser
- Optical frequency combs
- Quantum physics and engineering
- Time & Frequency instrumentation

## General Description

# Overview

The SK305 *Linear TEC Driver* was designed to operate with the SK484 *Temperature Controller* for high-performance thermal stabilization of laser diodes. In order to achieve low-noise operation, the SK305's circuitry relies on pure analog design, eliminating inherent broadband noise due to switched-mode topologies. The SK305 associated

to the SK484 Temperature Controller and SK657 Ultra-Low Noise Current Source are the ideal instruments for controlling low-power laser diodes when noise is a primary concern. Like all modular instruments of the SK-Series, the SK305 Linear TEC Driver can be operated stand-alone or within a platform where several modules can be assembled to configure a specific control or measurement system.



## **Functional Block Diagram**

Refer to the *Functional Block Diagram* available online for a synthetic presentation of the SK305.

## **User's Guide**

The last version of the SK305 *User's Guide* is available online at the product page.

### **Programming Guide**

The online *Programming Guide* provides a detailed description of the SK305's remote commands.

### **Remote Interface**

SK305's settings are changed through the remote interface. All instrument settings can also be queried *via* the remote interface. The module generates a status signal to report a specific event to the host computer. The SK305 can be operated either inside or outside the dedicated SPK-Series platform.

## **Front-Panel Display**

The front panel of the SK305 provides the user with minimal information about the status of the instrument.

## **Current Source**

In order to achieve low-noise operation, the SK305's circuitry relies on pure analog design. In-

deed, whereas laser diode temperature controllers are usually based on switched-mode TEC drivers for delivering high-power at maximum efficiency, the SK305 features a class-AB power amplifier to eliminate inherent broadband noise due to H-bridge topologies. The apparent limited current range of the SK305 compared to its high-power counterparts is actually not relevant for thermal control of low-power laser diodes where TEC currents almost never exceed the ampere level. Nevertheless, paralleling two or more SK305 modules allows to increase the output current handling.

# Current Control

The power transconductance amplifier is driven by the control voltage provided by the input summing amplifier. Indeed, the output current can be fully controlled by *i*) the manual control voltage provided by an internal DAC, ii) the external control input and iii) the feedforward input. These three signal sources can be also combined to provided the current control voltage.

## **Protections**

In order to protect the load from excessive current, the control voltage is applied to a programmable limiter prior driving the voltage-to-current converter. The compliance voltage is also monitored for the protection of sensitive loads. Both current limiting and over-voltage detection can be used to automatically shut the current source down, which can be useful in certain critical situations.



## **Specifications**

## **Current Source**

#### Topology

Linear, class-AB amplifier w/ digital control

#### **Output (TECO)**

#### **Bandwidth and Noise**

SSBW FPBW Density, RTO RMS, RTO

## 

Interface Impedance Range Gain, RTO Gain, FFW

SMA, UTB, EXP  $100 \text{ k}\Omega$   $\pm 5 \text{ V}$  +1 A/V-1 V/V to +1 V/V, 12-bi

10 kHz, small signal

 $\leq 1\,\mu\mathrm{A}/\sqrt{\mathrm{Hz}},\,f\geq 10\,\mathrm{Hz}$ 

TBD, full-power

#### Manual Control DAC

Range Resolution Precision -1000 mA to +10001 mA

1% of full-scale

FILL

## Protections

#### **Current Limitation**

| Positive   | $0 \mathrm{mA}$ to $+1000 \mathrm{mA}$ |
|------------|--|
| Negative   | $0 \mathrm{mA}$ to $-1000 \mathrm{mA}$ |
| Resolution | $1 \mathrm{mA}$                        |
| Accuracy   | TBD                                    |
|            |  |

#### **Over-Voltage Detection**

Positive Negative Resolution Accuracy 
 Detection

 0 mV to +5000 mV

 0 mV to 5000 mV

 1 mV

 TBD

## Monitoring

#### Monitoring Output (MONO)

Interface Impedance Max. Level SMA connector, UTB, EXP 100- $\Omega$  series  $\pm 10 \text{ V}$ 



## **General Characteristics**

This module is designed to be operated in laboratory environment.

## **Operating Temperature**

Range

 $+15\,^{\rm o}{\rm C}$  to  $+40\,^{\rm o}{\rm C}$  non-condensing

## **Host PC Communications**

| UART format | 9600 baud, 8-bit data        |
|-------------|------------------------------|
|             | 1 stop-bit, no flow control  |
| Interface   | DIN41612 backplane connector |

### Connectors

| Backplane | DIN41612-C96 | male |
|-----------|--------------|------|
|           |              |      |

| Expansion/Test<br>AIO<br>DIO | 40-pin PC/104 header<br>40-pin PC/104 header |
|------------------------------|--|
| MONO                         | SMA front-panel                              |

| SMA front-panel |
|-----------------|
| SMA front-panel |
| BNC front-panel |
|                 |

## **Front-Panel Indicators**

| RE, RA        | Remote interface error, activity |
|---------------|----------------------------------|
| ON            | Output status                    |
| MA            | Manual control                   |
| EX            | External control                 |
| $\mathbf{FF}$ | Feedforward control              |
| LM            | Current limiting                 |
| FD            | Fault detection                  |
|               |                                  |
|               | $\langle \rangle \rangle$        |
|               | $\mathbf{X}$                     |

## **Power Supply Inputs**

Analog Analog Power, digital Power

al  $-15 V \times 60 mA$  $+5 V \times 1 A$  $-5 V \times 1 A$ 

 $+15\,\mathrm{V}\times60\,\mathrm{mA}$ 

## Printed Circuit Board

Form factor Dimensions Technology

#### Eurocard. $100 \times 160 \times 1.6 \text{ mm}$ 4-layer, improved FR-4

## **Physical Properties**

Height Width Depth Weight Front-Panel

128.4 mm (3U) Single-wide, 20 mm (4HP) 174.5 mm TBD  $\approx 300$  g Anodized aluminium with rear conductive

## Warranty

One (1) year parts and labor on defects



## **Ordering Information**

## SK305 Module

The SK305 module can be ordered with different options.

Ordering Code SK305-FPSK305-NP

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### Accessories

No accessories are related to the SK305 module.



## **Document Identifier**

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## **Document Revision History**

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### **Important Notice**

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